Serial Number: 09/970,145 Filing Date: October 2, 2001

Title: METHOD AND SYSTEM OF TAPE AUTOMATED BONDING

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#### **REMARKS**

This responds to the Advisory Action dated February 9, 2006 and the Final Office Action dated September 13, 2003.

Claims 7 and 11 are amended. Claims 7-13 and 20-25 are pending in this application.

# §102 Rejection of the Claims

Claims 7-9, 11, 12 and 20-25 were rejected under 35 U.S.C. § 102(b) as being anticipated by Buckley, III, et al. (U.S. Patent No. 5,477,082).

#### Claims 7-9 and 20-22

Applicant has amended claim 7 to better describe the subject matter recited in the claim. Applicant believes claim 7 is not anticipated by the cited reference since the reference does not include each limitation recited in the claim. For instance, Applicant cannot find in the cited reference: an electrical component mounted on or above a surface of the IC chip and electrically connected to the IC chip by a lead on the TAB leadframe, the lead positioned along a surface of the TAB leadframe such that the lead extends from a point which is within an inner portion of the leadframe outward toward an outer portion of the leadframe such that the lead extends from the electrical component to a perimeter I/O of the IC chip, as recited in claim 7. In contrast, Buckley discusses a double-sided structure where the devices 56, 58A, 90 are connected to each other and contacts 24A of a leadframe through vias 22. (See Figs. 3 and 6 of Buckley). The vias 22 do not extend from a point which is within an inner portion of the leadframe outward toward an outer portion of the leadframe such that the lead extends from the electrical component to a perimeter I/O of the IC chip, as recited in claim 7.

Claims 8, 9, and 20-22 include each limitation of their parent claim and are therefore also not anticipated by the cited reference. Reconsideration and allowance is respectfully requested.

### Claims 11, 12, and 23-25

Applicant has amended claim 11 to better describe the subject matter recited in the claim. Applicant believes claim 11 is not anticipated by the cited reference since the reference does not include each limitation recited in the claim. For instance, Applicant cannot find in the cited

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reference: at least one of the plurality of leads extends within the ILB area so that the at least one lead has a first end located outside the ILB portion and a second end having a contact exposed interior to the ILB portion of the TAB structure and above a major surface of the IC chip, as recited in claim 11. Again, Buckley discusses a double-sided structure where the devices 56, 58A, and 90 are connected to each other and contacts 24A of a leadframe through vias 22. (See Figs. 3 and 6 of Buckley). The leads do not have first end located outside the ILB portion and a second end having a contact exposed interior to the ILB portion of the TAB structure. In contrast, they are merely vertical vias, as discussed above.

Claims 12 and 23-25 include each limitation of their parent claim and are therefore also not anticipated by the cited reference. Reconsideration and allowance is respectfully requested.

## §103 Rejection of the Claims

Claims 10 and 13 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Buckley, III, et al. (U.S. Patent No. 5,477,082) in view of admitted prior art (APA). Claims 10 and 13 include each limitation of their parent claims and are not obvious in view of the cited references for the reasons given above. Reconsideration and allowance is respectfully requested.

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# **CONCLUSION**

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney (612) 359-3267 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this day of October, 2006.

Name

Signature